

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re the Application of: Jeroslav Hynecek

Filed: June 6, 2001

Examiner: Ometz, David

Serial No. 09/874,032

Art Unit: 2615

Title: Low Feed Through-High
Dynamic Range Charge Detection
Using transistor Punch Through
Reset

File: ISE107

I certify that this correspondence is being deposited
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VA 22313-1450 on the date below.

9/21/05 
Date: John E. Vandigriff, Reg. No. 22,127

AMENDMENT UNDER 37 CFR §1.115

Hon. Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Official Action mailed July 11, 2005, please amend the above identified application as follows.